

FIG. 1

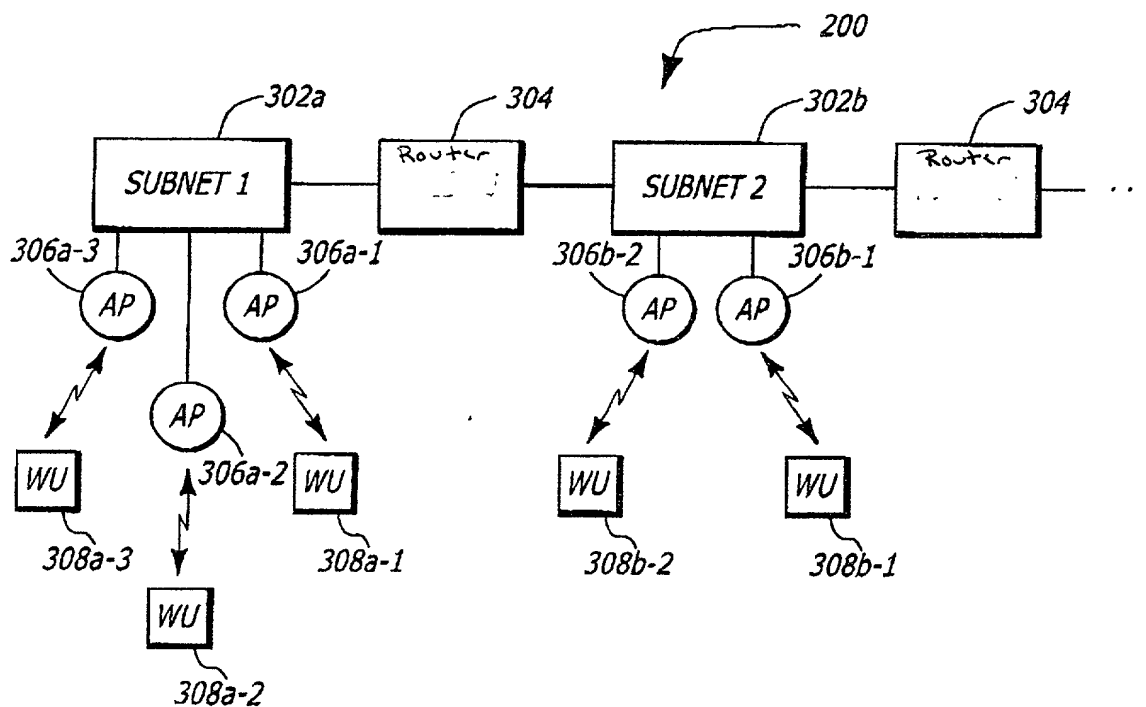


FIG. 3

The diagram illustrates the internal architecture of a network device, labeled **AP 106b**. At the top, a horizontal line represents a network interface, with a bidirectional arrow labeled **102** indicating communication with an external network. Below this, a dashed line connects to a **CAM** block (labeled **212**) and an **HAF** block (labeled **214**). The main processing core consists of **LOGIC 200** and **LOGIC 202**, connected by a large bidirectional arrow. **LOGIC 200** is connected to an **ADDR TABLE 204** and a **DEVICE MGMT MODULE 206**. The **DEVICE MGMT MODULE 206** is also connected to an **I/F** block (labeled **216**) and a **POWER AMP 209**. The **POWER AMP 209** is connected to an **ANTENNA 210**, which is part of a **WIRELESS TRANSCEIVER 208**. The **WIRELESS TRANSCEIVER 208** is connected to **LOGIC 202**. The **ANTENNA 210** is shown with three arrows pointing outwards, indicating wireless communication.

[illegible]

FIG. 4

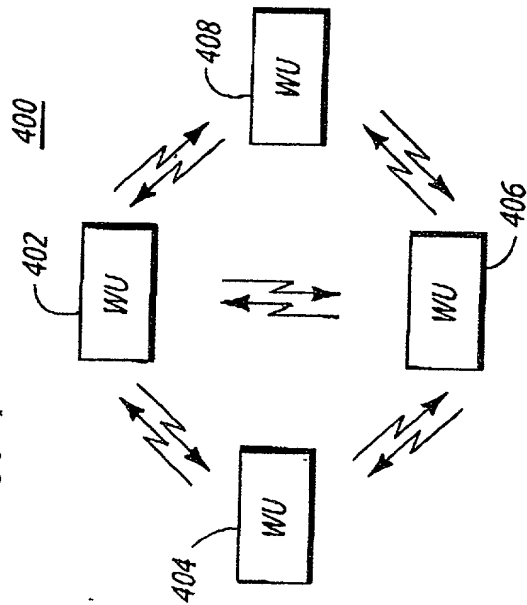


FIG. 5

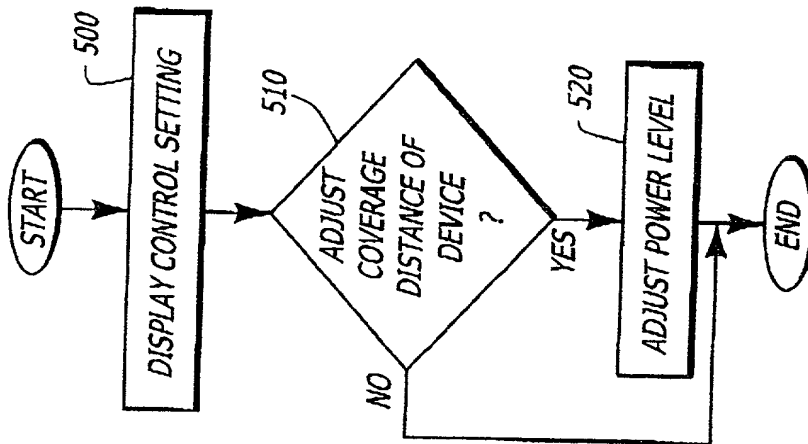
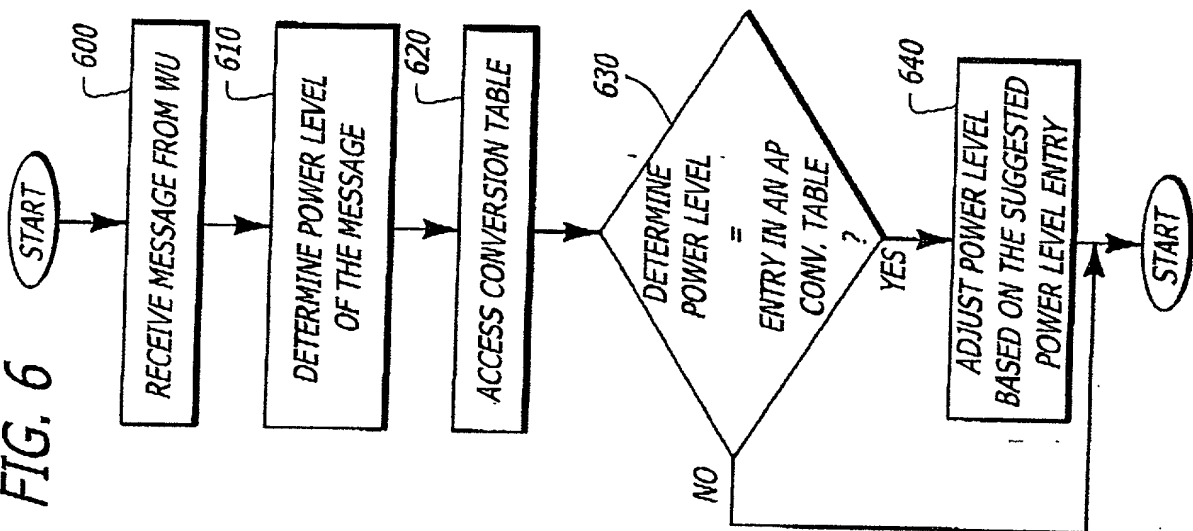


FIG. 6



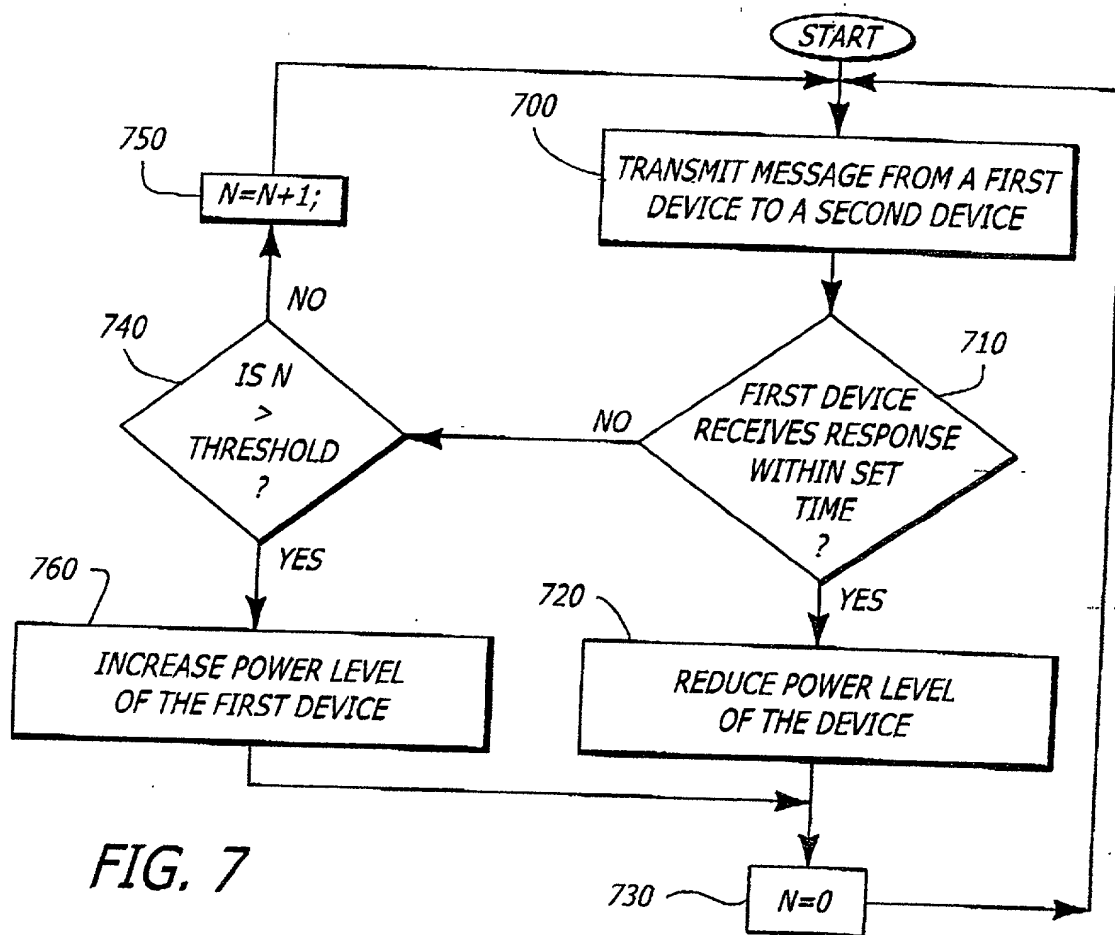


FIG. 7

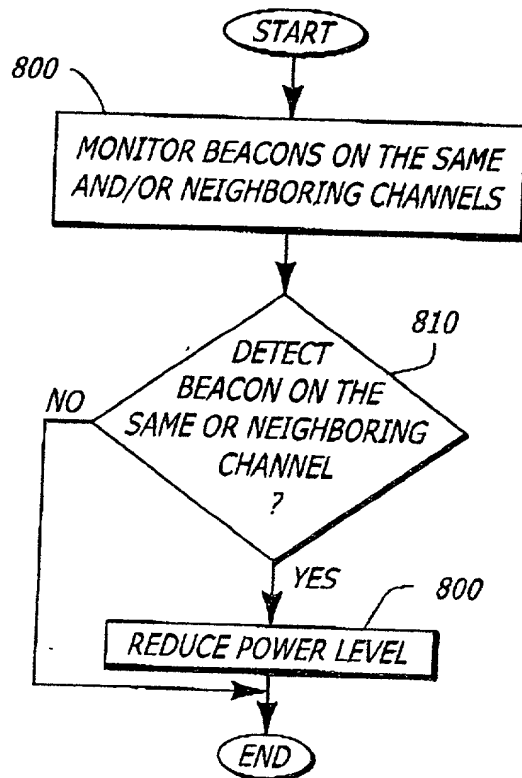


FIG. 8